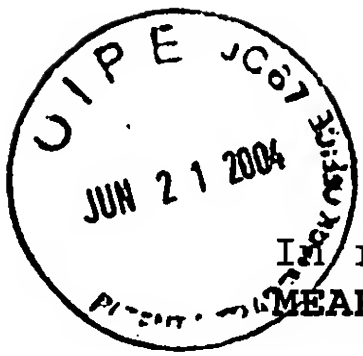


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

MEARS

Serial No. 10/717,370

Confirmation No. 6723

Filing Date: November 19, 2003

For: METHOD FOR MAKING
SEMICONDUCTOR DEVICE
INCLUDING BAND-ENGINEERED
SUPERLATTICE

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) Examiner: Nadine G. Norton
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) Art Unit: 1765
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) Attorney Docket: 62603_CON2
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PRELIMINARY AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to the calculation of fees and examination of
the present application, please enter the amendments and
remarks set out below.